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Title: **Method and Apparatus for Eliminating the Effects of Frequency Offsets in a Digital Communication System**

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Client Ref: TS-005

Title: **Method and Apparatus for Eliminating the Effects of Frequency Offsets in a Digital Communication System**

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ADDRESS OF CONCERN **3145 Porter Drive, Palo Alto, CA 94304**

I hereby declare that the above identified small business concern qualifies as a small business concern as defined in 13 CFR 121.12, and reproduced in 37 CFR 1.9(d), for purposes of paying reduced fees under Section 41(a) and (b) of Title 35, United States Code, in that the number of employees of the concern, including those of its affiliates, does not exceed 500 persons. For purposes of this statement, (1) the number of employees of the business concern is the average over the previous fiscal year of the concern of the persons employed on a full-time, part-time or temporary basis during each of the pay periods of the fiscal year, and (2) concerns are affiliates of each other when either, directly or indirectly, one concern controls or has the power to control the other, or a third party or parties controls or has the power to control both.

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METHOD AND APPARATUS FOR ELIMINATING THE EFFECTS OF FREQUENCY OFFSETS IN A DIGITAL COMMUNICATION SYSTEM

BACKGROUND OF THE INVENTION

1. Field of the Invention

The present invention relates to digital communications, and more particularly, to methods for correcting carrier frequency and sampling frequency at the transmitter to eliminate the effects of offsets in such frequencies.

2. Description of the Related Art

In a digital communication system composed of at least two transceivers, one serving as a transmitter and the other as a receiver, problems occur if the reference frequencies of the two transceivers are not exactly the same.

There are in general two sources of frequency offsets: carrier frequency offsets and sampling frequency offsets. Carrier frequency offsets result in the received signal being demodulated by a wrong carrier frequency, while sampling frequency offsets result in the data being sampled at the wrong time instants. Typically, the percentages of the carrier frequency offset and the sampling frequency offset would be the same if a single oscillator reference is used to generate the two frequencies at both the transmitter and the receiver.

The percentages of the two frequency offsets will be different if different oscillator references are used.

Conventionally, such frequency offsets are only detected and corrected during processing at the receiver end. For example, the receiver can employ a carrier frequency lock loop to determine the carrier frequency offset and a delay lock loop to determine the sampling frequency offset. Such mechanisms are only used to detect the frequency offset between the transmitter and the receiver, and to compensate for offset effects at the receiver end. See, for example: H. Meyr, M. Moeneclaey, and S. Fechtel, *Digital Communication Receivers*, Wiley-Interscience Publication, 1998; S. Kay, "A fast and accurate single frequency estimator," *IEEE Trans. on Acoustics, Speech, and Signal Processing*, December 1989; Viterbi and A. Viterbi, "Nonlinear estimation of PSK-modulated carrier phase with application to burst digital transmission," *IEEE Trans. on Information Theory*, July 1983; M. Fitz, "Further results in the fast estimation of a single frequency," *IEEE Trans. on Communications*, February 1994; and D. Messerschmitt, "Frequency detectors for PLL acquisition in timing and carrier recovery," *IEEE Trans. on Information Theory*, September 1979.

Conventional techniques for reducing the effects of frequency offsets at the receiving end have many shortcomings. For example, if narrow-band frequency division multiple access (FDMA) is used to provide multiple access from different users, the carrier frequency offsets in the reverse link (from end-

users to the base-station) might cause data to overlap in frequency at the base-station receiver. On the other hand, if time division multiple access (TDMA) is used to provide multiple access from different users in the reverse link, sampling frequency offsets might cause data to overlap in time at the base-station receiver.

In more advanced communication systems that employ either multi-user detection in a code division multiple access (CDMA) system, or multi-carrier modulation in an orthogonal frequency division multiplexing (OFDM) system, frequency offsets are particularly damaging in signal detection in the reverse link. For example, in CDMA systems where multiple user access is provided via multiple remote units, multi-user detection can be employed at the base station for interference rejection. See, for example, S. Verdu, *Multiuser Detection*, Cambridge University Press, 1998. However, the carrier frequency offsets introduced in the reverse link by the various remote units, if not corrected during transmission by the remote units, will destroy the stationary properties of the combined signal as received by the base station, thus greatly degrading the multi-user detection performance. Similarly, in an OFDM system, multiple frequency carriers are used to transmit data to and from multiple users. See, for example, B. Le Floch, M. Alard, and C. Berrow, "Coded Orthogonal Frequency Division Multiplex," *Proceedings of IEEE*, pp. 982-996, Vol. 83, No. 6, June 1995. If multiple remote users use different carrier frequencies to transmit data at the same time in an OFDM system, as in the

reverse link of a CDMA system, the frequency offsets in both carrier frequency and sampling frequency will cause the data from different users to overlap in both frequency and time, again greatly degrading the multi-carrier detection performance.

Accordingly, there remains a need in the art for techniques for reducing frequency offsets that improve the signal detection capability of the combined signals received from multiple remote units in a base station, and in general between any two transceivers. The present invention fulfills this need, among others.

SUMMARY OF THE INVENTION

Accordingly, an object of the present invention is to overcome the problems of the prior art, including the problems identified above.

Another object of the present invention is to improve signal detection capability of combined signals received from multiple remote units in a base station, and in general between any two transceivers.

Another object of the invention is to correct frequency offsets between transceivers, and particularly between a base station and a remote unit.

Another object of the invention is to correct carrier frequency offsets between transceivers, and particularly between a base station and a remote unit.

Another object of the invention is to correct sampling frequency offsets

between transceivers, and particularly a base station and a remote unit.

Another object of the invention is to correct for frequency offsets between transceivers, and particularly a base station and a remote unit, by adjusting a carrier frequency before transmission.

Another object of the invention is to correct for frequency offsets between transceivers, and particularly a base station and a remote unit, by adjusting a sampling frequency before transmission.

To achieve these objects and others, the invention aims at eliminating the effects of frequency offsets between transceivers by adjusting frequencies used during transmission. In this invention, methods for correcting the carrier frequency and the sampling frequency during transmission are provided, including both digital and analog, and closed and open loop implementations of such methods. The receiver determines the relative frequency offset between the transmitter and the receiver, and uses this information to correct this offset when the receiver transmits its data to the original transmitter in the return path, so that the signal received by the original transmitter is in frequency lock with the original transmitter's local frequency reference.

BRIEF DESCRIPTION OF THE DRAWINGS

These and other objects and advantages of the present invention, along with the best mode for practicing it, will become apparent to those skilled in the art after considering the following detailed specification, together with the

accompanying drawings wherein:

FIG. 1 illustrates a communication system in accordance with the invention;

FIG. 2 illustrates a receiver in a remote unit that detects carrier frequency offsets in accordance with a first embodiment of the invention;

FIG. 3 illustrates a transmitter in a remote unit that corrects for carrier frequency offsets in accordance with a first embodiment of the invention;

FIG. 4 illustrates a receiver in a remote unit that detects sampling frequency offsets in accordance with a second embodiment of the invention;

FIG. 5 illustrates a transmitter in a remote unit that corrects for sampling frequency offsets in accordance with a second embodiment of the invention;

FIG. 6 illustrates a transceiver in a remote unit that corrects for carrier frequency offsets in accordance with a third embodiment of the invention;

FIG. 7 illustrates a receiver in a remote unit that detects sampling frequency offsets in accordance with a fourth embodiment of the invention;

FIG. 8 illustrates an alternative receiver in a remote unit that detects sampling frequency offsets in accordance with a fourth embodiment of the invention;

FIG. 9 illustrates a receiver in a remote unit that detects sampling frequency offsets in accordance with a fourth embodiment of the invention; and

FIG. 10 illustrates a transmitter in a remote unit that corrects for

sampling frequency offsets in accordance with a fourth embodiment of the invention.

DETAILED DESCRIPTION OF THE INVENTION

FIG. 1 illustrates a preferred embodiment in which a base station (transceiver 120) communicates with multiple remote (e.g. hand-held and/or mobile) units (transceiver 100-1, transceiver 100-2, etc.).

In the discussion below, CDMA is preferred as the carrier modulation technique to provide multiple user access and multi-user detection is preferably employed at the base station for interference rejection. The carrier and sampling frequency offsets introduced by various remote units, if not appropriately corrected during transmission, will destroy the stationary properties of the combined signal as received by the base station. According to an aspect of the invention, therefore, each remote unit 100 corrects the frequency offsets during transmission as will be described in more detail below, thus creating a low IF modulation and interpolation effect, before sending the signal to the analog front-end circuitry. Although the invention is particularly useful for CDMA modulation techniques, the invention is not limited to this example, but should be particularly useful for many other modulation techniques where frequency offsets can introduce difficulties such as multi-user detection systems employing antenna diversity or smart antennas. In fact, any modulation or diversity schemes that rely on accurate frequency and/or

time resolution can benefit from this invention, which include, but not limited to, previously mentioned OFDM systems, discrete multiple tone (DMT) systems, multiple antenna systems, narrow-band FDMA systems, or TDMA systems. Moreover, although the invention is particularly useful and generally described herein with reference to multi-user systems with a base station and multiple remote units, it should be apparent that the principles of the invention can be extended to two or any number of transceivers in mutual communication.

A first preferred embodiment of the invention provides for digital correction of carrier frequency offsets. In this embodiment, a receiver in a remote unit 100 employs a frequency-lock loop 202, as illustrated in FIG. 2, to detect the carrier frequency offset between the base station and the remote unit. More particularly, the received signal from the base station, after down-conversion mixing to the baseband and A/D conversion, is digitally shifted in frequency to DC by multiplying the received signal with a complex sinusoidal (or simple sinusoidal ~~for real signal only~~) using frequency shift block 204. The frequency of the complex sinusoidal is the carrier frequency offset between the base-station and the remote unit. The actual frequency used in the multiplication is determined by carrier frequency control block 206. This is done by either feed-forward or feed-back frequency lock loop (FLL 202) using the correlation between the received and the detected data.

FLL 202 can be implemented by any one of many conventional frequency lock loop methods including the publications referred to above, and a detailed

description thereof is not considered necessary for an understanding of the present invention. Frequency shift block 204 can be implemented by, for example, a dedicated-hardware complex or simple multiplier or software executing on a digital signal processor. Frequency control block 206 can be implemented by, for example, dedicated-hardware control circuits or software executing on a digital signal processor. The designs of both blocks are commonly known by those skilled in the art.

When there is a difference between the carrier frequency used by remote unit 100 in performing the down-conversion mixing and the carrier frequency used by the base station, the baseband signal will have an offset. This offset is detected and effectively cancelled by FLL 202. The resulting baseband signal is then usable for data detection by block 208, which data detection can be performed using conventional baseband demodulation techniques such as QPSK, for example. The offset detected by FLL 202 is supplied to carrier frequency control 206. Further offset information can be determined during data detection by continuous comparison of the received signal and detected signal in block 208, which information can be used to further refine the remote unit carrier frequency.

It will be understood by those skilled in the art that other remote unit receiver or transceiver components are possible in addition to those described above and below. However, a detailed description thereof is not necessary for an understanding of the present invention.

When the remote unit transmits data to the base station, the remote unit performs a frequency shift in the digital domain before sending the data to the DAC. More particularly, in the remote unit transmitter as illustrated in FIG. 3, data to be transmitted is assembled in packets (in accordance with the communication protocol used by the system) by assembler 302. From there, the assembled data is digitally shifted in frequency by block 304 using the carrier offset information detected by the receiver in FIG. 2. The shifted digital data is then converted to analog by DAC 306 and transmitted by transmitter 308.

Frequency shift block 304 performs a multiplication of the assembled data and a complex sinusoidal (or a simple sinusoidal if the data is real only) and can be implemented by, for example, a dedicated-hardware multiplier or software executing on a digital signal processor. Transmitter 308 performs filtering, up-conversion mixing and amplification before sending the data to the antenna for transmission.

A second preferred embodiment of the invention provides for digital correction of sampling frequency offsets. FIG. 4 illustrates a remote unit receiver that includes a delay-lock loop to accurately determine the correct sampling point of the received signal. If the received signal is generated at a rate faster than the local sampling frequency, i.e. the base station DAC clock is faster than the remote unit ADC clock, then occasionally two samples will be received in one ADC clock cycle. If the received signal is generated at a rate

slower than the ADC sampling frequency, i.e. the base station DAC clock is slower than the remote unit ADC clock, then occasionally no sample will be received in one ADC clock cycle. In the conventional design, the correct sampling point relative to the ADC clock, $\delta\tau$, detected by the delay-lock loop, is used to interpolate the correct sample from the ADC output.

As shown in FIG. 4, a remote unit receiver in this embodiment of the invention includes a timing acquisition unit 402, interpolation filter 404, data detection block 406, and delay-lock loop 408. Timing acquisition unit 402 samples the received data based on the timing information from delay-lock loop 408; delay-lock loop 408 compares the sampled data and detected data and generates the timing offset $\delta\tau$; interpolation filter 404 filters the incoming samples and generates the interpolated sample at $\delta\tau$ offset in time from the original sample; data detection block 406 performs the final signal detection. These components can be implemented, for example, by either dedicated-hardware or software executing on a digital signal processor.

An example of a remote unit transmitter that can be used to transmit a sampling frequency-corrected signal in this embodiment of the invention is illustrated in FIG. 5. As illustrated in FIG. 5, this embodiment of the invention uses the detected $\delta\tau$ (from delay-lock loop 408, for example) to perform sampling rate conversion on the data to be transmitted by way of a digital lowpass filter 506.

More particularly, the remote unit 100 modulates the data (after being assembled into packets by assembler 502 in accordance with the communication protocol) in data modulation block 504 using, for example, QPSK. 506, the rate-conversion and interpolation block, then interpolates data at the appropriate sampling points based on the sampling frequency of the base station which is determined from the detected $\delta\tau$. The interpolation filter 506 can be any low-pass filter with a reasonable frequency response, but preferably a pulse-shaping filter so that spectrum-shaping can be performed simultaneously. The interpolated data is then converted to an analog signal using a DAC 508, clocked at the remote unit's sampling frequency. The analog signal will thus display the timing properly synchronized with the sampling frequency at the base station. The analog signal is then filtered, up-converted and amplified in the analog front-end circuitry 510 before being sent to the antenna for transmission.

A third preferred embodiment of the present invention provides for analog correction of carrier frequency offsets, and is illustrated in FIG. 6.

In this embodiment, a closed-loop system corrects the frequency offset between the remote unit and the base station carrier frequencies. The system of this embodiment functions as follows. In the remote unit, a frequency-locked loop 602, which can be the same as FLL 202 in FIG. 2, detects the frequency offset between the two carrier frequencies and generates a signal V_c that is proportional to the difference between the two carrier frequencies. This

signal is then used to adjust the capacitance of capacitor C_c, which in turn changes the resonance frequency of crystal oscillator 604 in a direction that corrects the frequency offset. It should be noted that capacitor C_c can be placed either in parallel or in series with the crystal depending on the oscillator's architecture, and that other variably adjustable passive or active devices can be used. The reference frequency provided by oscillator 604 is supplied to frequency synthesizer 606 which produces the adjusted carrier frequency that is modulated before transmission to the base station. Since the carrier frequency is adjusted in transmission, the base station will properly demodulate the data from the reverse link signal. A description for the operation of the frequency synthesizer block can be found in "RF Microelectronics" by Behzad Razavi, Prentice Hall, 1998.

A fourth preferred embodiment of the present invention provides for analog correction of sampling frequency offsets, for example by varying the sampling clock of the analog-to-digital converter (ADC) and/or digital-to-analog converter (DAC). More particularly, the sampling frequency and phase offsets can be corrected in the remote unit in the analog domain by varying the sampling phases of the ADC of a receiver or the DAC of a transmitter. In accordance with one aspect of the invention, phase interpolation can be used to generate the sampling clock of an ADC for receive, and phase interpolation can be used to generate the sampling clock of a DAC for transmit.

Receivers of sampled data systems (such as radio) require some form of timing recovery mechanism to align the sampling frequency and phase of the receiver to that of the transmitter. This operation can be done in the analog domain by adjusting the sampling clock of the ADC as described in, for example, P. Roo, et al., International Solid State Circuits Conference 1998, pp. 392-393 and T. Lee, et. al., International Solid State Circuits Conference 1994, pp. 300-301.

FIG. 7 shows an analog timing recovery system in accordance with the analog implementation described above that uses a phase detector 702, loop filter 704 and a voltage-controlled oscillator (VCO) 706 to generate the desired sampling clock for the ADC 708 of the receiver. The phase detector 702, loop filter 704, and VCO 706 form a conventional phase/frequency-locked loop that keeps the phase and frequency of the sampled input signal equal to that of the VCO output. The phase detector 702 compares the phase/frequency of the sampled input signal and the VCO output, loop filter 7O4 is a low-pass filter and can be implemented using R-C circuits. The output of the loop filter is a dc (or low frequency signal) that sets the phase/frequency of the VCO output so that it is equal to that of the sampled input signal.

An alternative implementation of this embodiment is shown in FIG. 8 whereby the phase detector 802, loop filter 804, and VCO 806 are implemented as digital circuits. A typical way to implement a digital VCO is to include a divider 808 that derives the sampling clock from an external reference clock

F_{EXT} that is M times faster than the desired sampling clock (where M can be selected from a number of alternative integers). The precision of the timing recovery is quantized to the nearest sampling edge of the external reference clock F_{EXT}.

In high speed communication systems where the sampling frequency can be over 100MHz, however, the design of a digital VCO with an external reference clock that is M times faster is not desirable. According to an aspect of the present invention, a technique is provided to perform timing recovery using digital circuits without the need for a reference clock that is M times faster than the sampling frequency. The basic idea to generate the M phases of the sampling clock by using phase interpolator, which can be implemented using techniques such as those described in T. Knotts, et. al, International Solid State Circuits Conference 1994, pp. 58-59 and D. Chu and T. Knotts, U.S. Patent No. 5,166,959.

A closed loop system for digital timing recovery of sampling frequency offsets in accordance with this aspect of the invention is illustrated in FIGs. 9 and 10.

As shown in FIG. 9, the timing recovery circuit consists of a digital phase detector 902 and a digital loop filter 904, which can be the same as those described above. Digital VCO 908 in this example of the invention, however, includes a phase interpolator 910 which generates M phases of the sampling clock using techniques such as those described in T. Knotts, et. al,

International Solid State Circuits Conference 1994, pp. 58-59 and D. Chu and T. Knotts, U.S. Patent No. 5,166,959. The phase detector 902 compares the digitized input signal (at Nyquist rate) with the sampling frequency. This phase comparison can be done using a digital multiplier or more simply with adders if the characteristics of the incoming signal are known. The loop filter 904 typically consists of at least one integrator (accumulator) and compensation network. State machine 906 monitors the frequency and phase offsets of the input sampling signal and can be implemented by using standard digital logic such as those described in (or any other logic text on state machines) Hill & Peterson, Introduction to Switching Theory & Logical Design, 2nd Ed., John Wiley & Sons, 1981.

The timing recovery loop works as follows. The phase of the incoming signal is compared to the sampling clock. The difference in phase is accumulated by loop filter 904 and then used to produce a control signal that selects the most appropriate phase of the sampling clock produced by interpolator 910. Since the operation is a closed loop system, the phase adjustment need only provide the direction of the phase change not the absolute phase change.

The frequency offset adjustment process can be illustrated with the following example. If the receiver clock Fs is 10% faster than the transmitter clock and M is chosen to be 10, the timing recovery logic should reduce the receiver clock period by 1/M, i.e. one period after every 10 Fs periods. The

resultant receiver Fs should thus have the same average frequency as the transmitter frequency. The error due to the discrete choice of sampling phases can usually be tolerated as long as M is sufficiently large. The use of phase interpolation allows an implementation that does not require an external high frequency clock.

The phase interpolation approach described above can be applied to the transmitter to correct for frequency offsets during transmission in accordance with the invention. In this approach, the average change in the phase selection algorithm in FIG. 9 during the receive operation is used for transmit. For example, if the receiver timing recovery reduces the sampling clock period Fs by 1/M, or one period for every 10 Fs periods, the transmitter can use the same phase change for the transmitted data as shown in FIG. 10. The receive circuit in FIG. 9 preferably includes an additional state machine 906 to monitor the control signal to the digital VCO 908 in order to record the frequency and amplitude of the phase changes. This information is provided to the transmit circuit in FIG. 10 and used by state machine 1002 to control the phase selection of the digital VCO 1004 consisting of phase interpolator 1006 to adjust the sampling frequency for DAC 1008 so that the transmit signal will have the same average sampling frequency as the received signal.

It should be apparent that although the frequency correction operations of the invention have been generally described hereinabove as taking place in the transceiver of a remote unit, that such operations can also take place in the

transceiver of a base station, or in any transceiver or transmitter of a first unit in communication with another transceiver of transmitter of a second unit where a shared reference frequency or commonly used frequency is used by the first and second units.

Moreover, other embodiments of the invention are possible. For example, the transmitter-corrected frequency offset scheme can be applied to any digital communication system for better performance or lower-cost implementations. It is especially suited for applications that explore diversity to facilitate multiple access. Some such examples are multi-user detection systems employing antenna diversity or smart antennas, multi-carrier OFDM, DMT, etc. This invention allows the transmitted signals from multiple sources to be frequency-locked to the receiver, so that the signals from multiple sources are synchronized in both frequency and time.

Other embodiments of this invention can also include any point-to-point or broadcast channels, such as those used in ADSL or cable modem systems.

As a further alternative, the information on the frequency offsets can be sent from the receiver, after it has been detected, to the transmitter so that the transmitter can adjust its carrier frequency and/or sampling frequency accordingly for next transmission. For example, to reduce the hardware complexity or power consumption of a first transceiver, such as a remote unit, a second transceiver in communication therewith, e.g., a base station, can detect the frequency offset relative to the first transceiver, and send the offset

information to the first transceiver in the forward link. The first transceiver, after receiving the offset information, probably through a low-rate channel or a broadcast channel, will correct the frequencies as described in this invention. This is the scheme for closed loop frequency control.

The offset information can also be sent from the receiver of a first transceiver, to the transmitter of a second transceiver, for the second transceiver to correct its frequencies during receiving. For example, to reduce the hardware complexity of a first transceiver such as a remote unit, the sampling frequency offset information can be sent to a second transceiver such as a base station. The second transceiver, after receiving the offset information, will interpolate the received data accordingly, which enhances the detection capabilities without requiring the first transceiver to perform sampling rate conversion.

Although the present invention has been described in detail with reference to the preferred embodiments thereof, those skilled in the art will appreciate that various substitutions and modifications can be made to the examples described herein while remaining within the spirit and scope of the invention as defined in the appended claims.

What is claimed is:

1. A device adapted to be used in a communication system in which a first unit communicates with a second unit using a common frequency, the device comprising:

means for detecting an offset between the common frequency used by the first unit and the second unit in a first signal transmitted by the first unit and received by the second unit;

means for adjusting the common frequency in accordance with the offset in a second signal to be transmitted by the second unit and to be received by the first unit so that the effects of the offset to be perceived by the first unit will be substantially reduced.

2. A device according to claim 1, wherein the common frequency is a carrier frequency.

3. A device according to claim 1, wherein the common frequency is a sampling frequency.

4. A device according to claim 2, wherein the means for detecting the offset includes means for performing a correlation on a digital representation of the first signal so as to lock onto the offset in the carrier frequency.

5. A device according to claim 2, wherein the means for adjusting the common frequency includes a means for digitally shifting data in frequency to be transmitted in accordance with the carrier frequency and the offset.

6. A device according to claim 3, wherein the means for detecting the offset includes means for performing a variable delay on a digital representation of the first signal so as to lock onto the offset in the sampling frequency.

7. A device according to claim 3, wherein the means for adjusting the common frequency includes means for digitally interpolating data to be transmitted in accordance with the sampling frequency and the offset.

8. A device according to claim 2, wherein the means for detecting the offset includes means for locking onto the offset in the carrier frequency and for producing an output signal corresponding thereto.

9. A device according to claim 8, wherein the means for adjusting the common frequency includes means for variably adjusting a reference frequency output by a crystal oscillator in accordance with the output signal generated by the locking means.

10. A device according to claim 3, wherein the means for detecting the offset includes:

means for sampling the first signal;

means for generating an output frequency corresponding to the common sampling frequency having a desired phase and frequency in accordance with a control signal;

means for comparing the sampled first signal and the output frequency so as to lock onto the offset in the sampling frequency; and

means for adjusting the control signal in accordance with the offset.

11. A device according to claim 10, wherein the means for generating the output frequency includes means for selecting one of M phases of the common sampling frequency in accordance with the control signal.

12. A device according to claim 3, wherein the means for adjusting the common frequency includes means for generating an output frequency corresponding to the common sampling frequency having a desired phase and frequency.

13. A device according to claim 10, wherein the means for adjusting the common frequency includes means for generating a second output frequency corresponding to the common sampling frequency having a second desired phase

and frequency.

14. A device according to claim 13, wherein the means for generating the output frequency includes means for selecting one of M phases of the common sampling frequency in accordance with the control signal.

15. A method adapted to be used in a communication system in which a first unit communicates with a second unit using a common frequency, the method comprising:

detecting an offset between the common frequency used by the first unit and the second unit in a first signal transmitted by the first unit and received by the second unit;

adjusting the common frequency in accordance with the offset in a second signal to be transmitted by the second unit and to be received by the first unit so that the effects of the offset to be perceived by the first unit will be substantially reduced.

16. A method according to claim 15, wherein the common frequency is a carrier frequency.

17. A method according to claim 15, wherein the common frequency is a sampling frequency.

18. A method according to claim 16, wherein the step of detecting the offset includes performing a correlation on a digital representation of the first signal so as to lock onto the offset in the carrier frequency.

19. A method according to claim 16, wherein the step of adjusting the common frequency includes digitally shifting data in frequency to be transmitted in accordance with the carrier frequency and the offset.

20. A method according to claim 17, wherein the step of detecting the offset includes performing a variable delay on a digital representation of the first signal so as to lock onto the offset in the sampling frequency.

21. A method according to claim 17, wherein the step of adjusting the common frequency includes digitally interpolating data to be transmitted in accordance with the sampling frequency and the offset.

22. A method according to claim 16, wherein the step of detecting the offset includes locking onto the offset in the carrier frequency and producing an output signal corresponding thereto.

23. A method according to claim 22, wherein the step of adjusting the

common frequency includes variably adjusting a reference frequency output by a crystal oscillator in accordance with the output signal generated by the locking means.

24. A method according to claim 17, wherein the step of detecting the offset includes:

sampling the first signal;

generating an output frequency corresponding to the common sampling frequency having a desired phase and frequency in accordance with a control signal;

comparing the sampled first signal and the output frequency so as to lock onto the offset in the sampling frequency; and

adjusting the control signal in accordance with the offset.

25. A method according to claim 24, wherein the step of generating the output frequency includes selecting one of M phases of the common sampling frequency in accordance with the control signal.

26. A method according to claim 17, wherein the step of adjusting the common frequency includes generating an output frequency corresponding to the common sampling frequency having a desired phase and frequency.

27. A method according to claim 24, wherein the step of adjusting the common frequency includes generating a second output frequency corresponding to the common sampling frequency having a second desired phase and frequency.

28. A method according to claim 27, wherein the step of generating the output frequency includes selecting one of M phases of the common sampling frequency in accordance with the control signal.

29. A device adapted to be used in a first unit that can communicate with a second unit using a common carrier frequency, the device comprising:

a frequency lock loop that is coupled to receive a digital representation of a first signal transmitted by the second unit, the frequency lock loop being adapted to detect a carrier frequency offset in the first signal and to produce offset information corresponding thereto; and

a frequency shift block that is coupled to receive the offset information and data to be transmitted by the first unit in a second signal to be received by the second unit, the frequency shift block being adapted to digitally shift the data in frequency in accordance with the common carrier frequency and the carrier frequency offset so that the effects of the carrier frequency offset to be perceived by the second unit will be substantially reduced.

30. A device adapted to be used in a first unit that can communicate with a second unit using a common sampling frequency, the device comprising:

a delay lock loop that is coupled to receive digitally sampled data of a first signal transmitted by the second unit, and digitally detected data of the first signal, the delay lock loop being adapted to detect a sampling frequency offset in the first signal based on the received data and to produce offset information corresponding thereto; and

a digital low-pass filter that is coupled to receive the offset information and data to be transmitted by the first unit in a second signal to be received by the second unit, the digital low-pass filter being adapted to digitally interpolate the data to be transmitted in accordance with the common sampling frequency and the sampling frequency offset so that the effects of the sampling frequency offset to be perceived by the second unit will be substantially reduced.

31. A device adapted to be used in a first unit that can communicate with a second unit using a common carrier frequency, the device comprising:

a frequency lock loop that is coupled to receive a digital representation of a first signal transmitted by the second unit, the frequency lock loop being adapted to detect a carrier frequency offset in the first signal and to produce an analog offset signal corresponding thereto;

a crystal oscillator that supplies a reference frequency for modulating a second signal to be perceived by the second unit in accordance with

the common carrier frequency; and

a variably adjustable device coupled to receive the offset signal and to the crystal oscillator, the variably adjustable device being adapted to adjust the reference frequency of the crystal oscillator in accordance with the offset signal so that the effects of the carrier frequency offset in the second signal to be perceived by the second unit will be substantially reduced.

32. A device adapted to be used in a first unit that can communicate with a second unit using a common sampling frequency, the device comprising:

a voltage-controlled oscillator that generates an output signal having a frequency and phase in accordance with a control signal;

a phase detector that is coupled to receive a sampled signal of a first signal transmitted by the second unit, and the output signal from the voltage-controlled oscillator, the phase detector being adapted to detect a sampling frequency offset in the first signal based on a comparison of the received signals and to cause the control signal to be adjusted in response to the comparison so that the effects of the sampling frequency offset in the first signal to be perceived by the first unit will be substantially reduced;

a digital-to-analog converter that receives data to be transmitted in a second signal to be received by the second unit in accordance with the common sampling frequency; and

a second voltage-controlled oscillator that produces the common

sampling frequency in response to a second control signal that has been adjusted in accordance with the comparison performed by the phase detector so that the effects of the sampling frequency offset in the second signal to be perceived by the second unit will be substantially reduced.

33. A device adapted to be used in a first unit that can communicate with a second unit using a common sampling frequency, the device comprising:

a voltage-controlled oscillator that generates an output signal having a frequency and phase in accordance with a control signal; and

a phase detector that is coupled to receive a sampled signal of a first signal transmitted by the second unit, and the output signal from the voltage-controlled oscillator, the phase detector being adapted to detect a sampling frequency offset in the first signal based on a comparison of the received signals and to cause the control signal to be adjusted in response to the comparison so that the effects of the sampling frequency offset in the first signal to be perceived by the first unit will be substantially reduced;

wherein the voltage-controlled oscillator includes a phase interpolator that generates a selected one of M phases of the common sampling frequency, the selected one being determined by the control signal.

34. A device adapted to be used in a communication system in which a first unit communicates with a second unit using a common frequency, the

device comprising:

means for detecting an offset between the common frequency used by the first unit and the second unit in a first signal transmitted by the first unit and received by the second unit;

means for communicating information corresponding to the detected offset from the second unit to the first unit;

means for adjusting the common frequency in accordance with the offset in a second signal to be transmitted by the first unit and to be received by the second unit so that the effects of the offset to be perceived by the second unit will be substantially reduced.

35. A device adapted to be used in a communication system in which a first unit communicates with a second unit using a common frequency, the device comprising:

means for detecting an offset between the common frequency used by the first unit and the second unit in a first signal transmitted by the first unit and received by the second unit;

means for communicating information corresponding to the detected offset from the second unit to the first unit;

means for adjusting the common frequency in accordance with the offset in a second signal to be transmitted by the second unit and to be received by the first unit so that the effects of the offset to be perceived by the first unit

will be substantially reduced.

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ABSTRACT OF THE DISCLOSURE

The present invention aims at eliminating the effects of frequency offsets between two transceivers by adjusting frequencies used during transmission. In this invention, methods for correcting the carrier frequency and the sampling frequency during transmission are provided, including both digital and analog implementations of such methods. The receiver determines the relative frequency offset between the transmitter and the receiver, and uses this information to correct this offset when the receiver transmits its data to the original transmitter in the return path, so that the signal received by the original transmitter is in sampling and carrier frequency lock with the original transmitter's local frequency reference.

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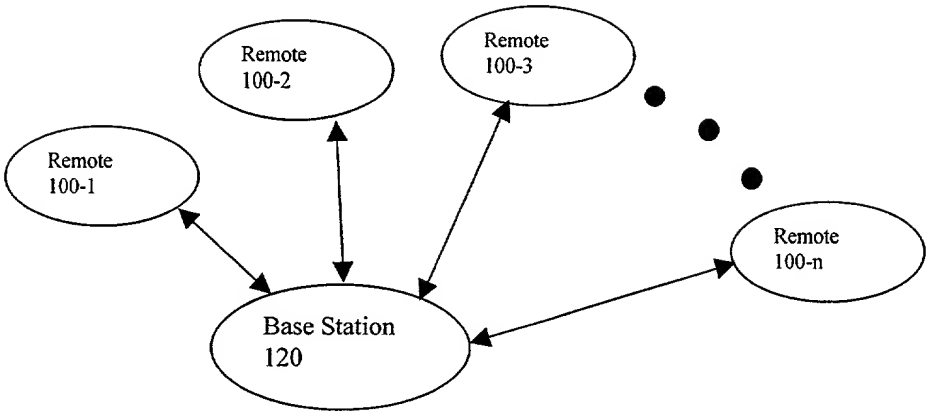
Remote
100-2
Remote
100-3
Remote
100-1
Remote
100-n
Base Station
120

FIG. 1

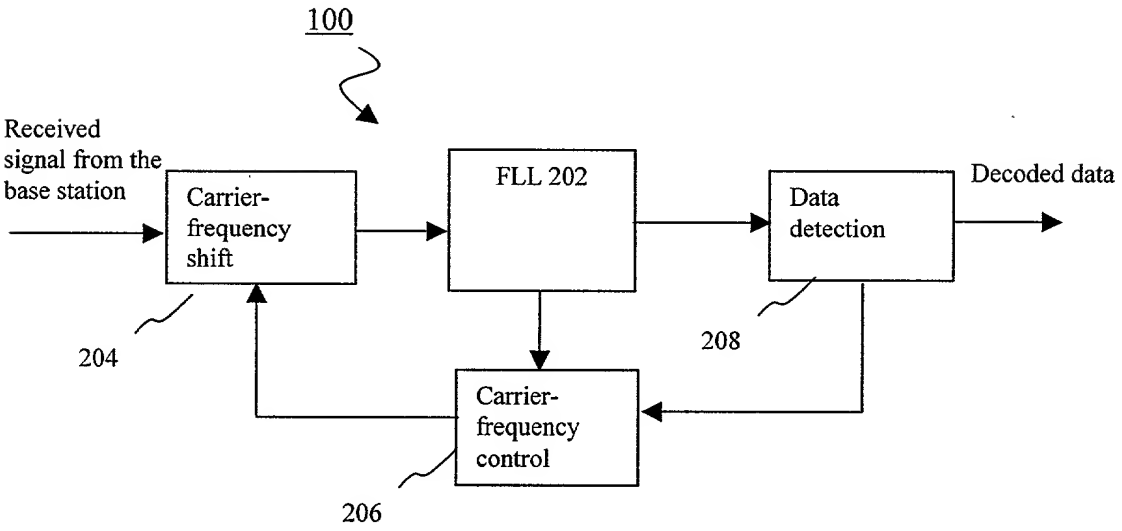
100
Received
signal from the
base station
Carrier-
frequency
shift
FLL 202
Data
detection
Decoded data
204
208
Carrier-
frequency
control
206

FIG. 2

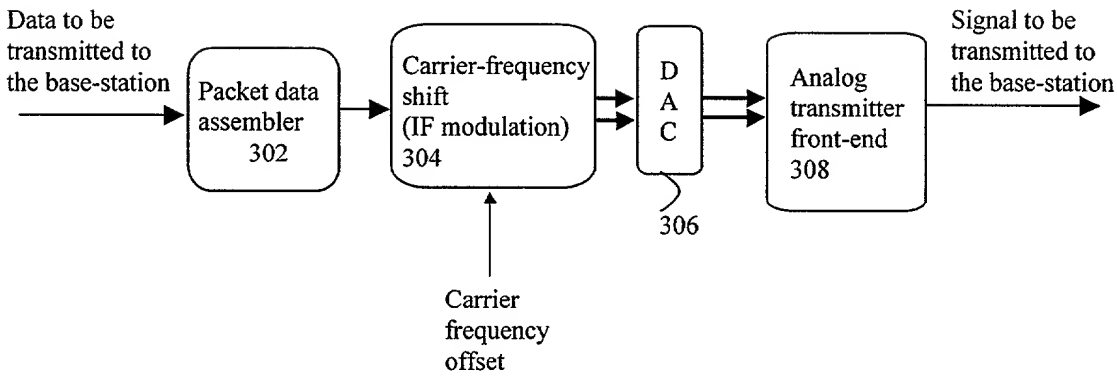
Data to be transmitted to the base-station
Packet data assembler 302
Carrier-frequency shift (IF modulation) 304
D A C
306
Analog transmitter front-end 308
Signal to be transmitted to the base-station
Carrier frequency offset

FIG. 3

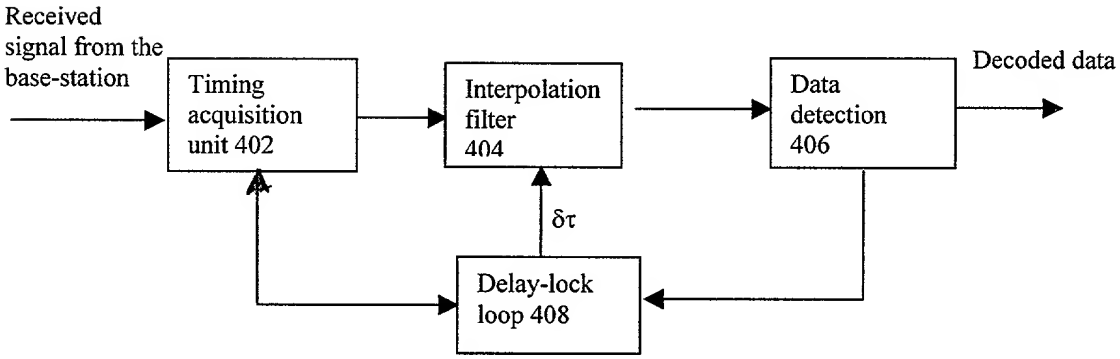
Received signal from the base-station
Timing acquisition unit 402
Interpolation filter 404
Data detection 406
Decoded data
δτ
Delay-lock loop 408

FIG. 4

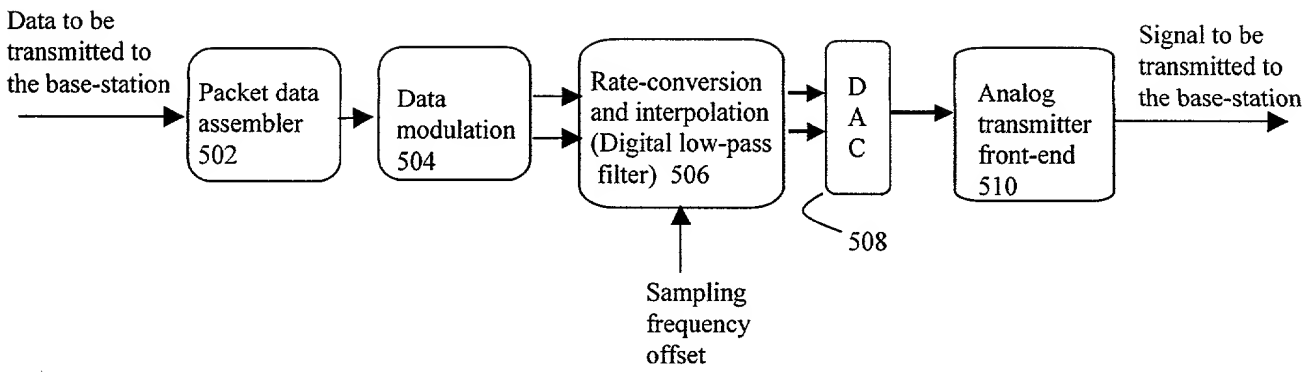
Data to be transmitted to the base-station
Packet data assembler 502
Data modulation 504
Rate-conversion and interpolation (Digital low-pass filter) 506
D A C
508
Analog transmitter front-end 510
Signal to be transmitted to the base-station
Sampling frequency offset

FIG. 5

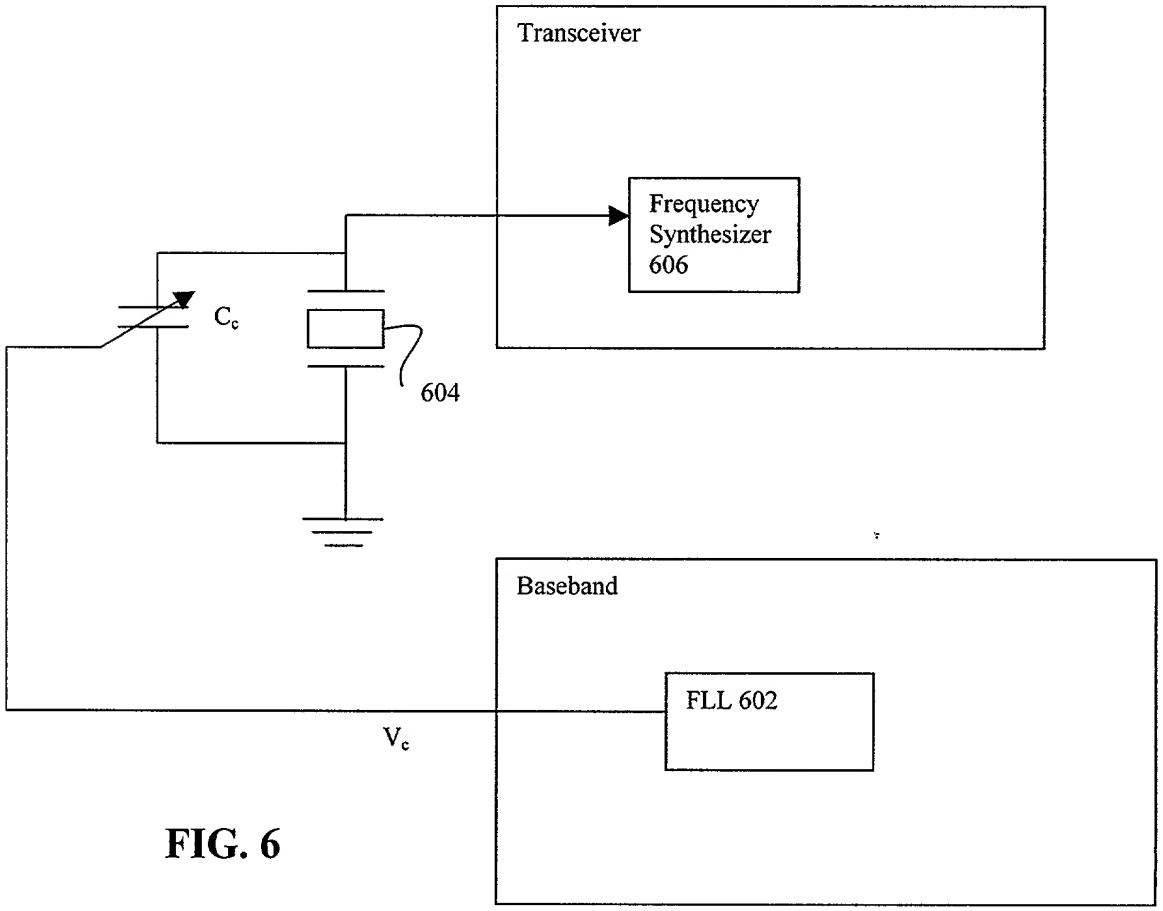
Transceiver
Frequency Synthesizer 606
C_c
604
Baseband
FLL 602
V_c

FIG. 6

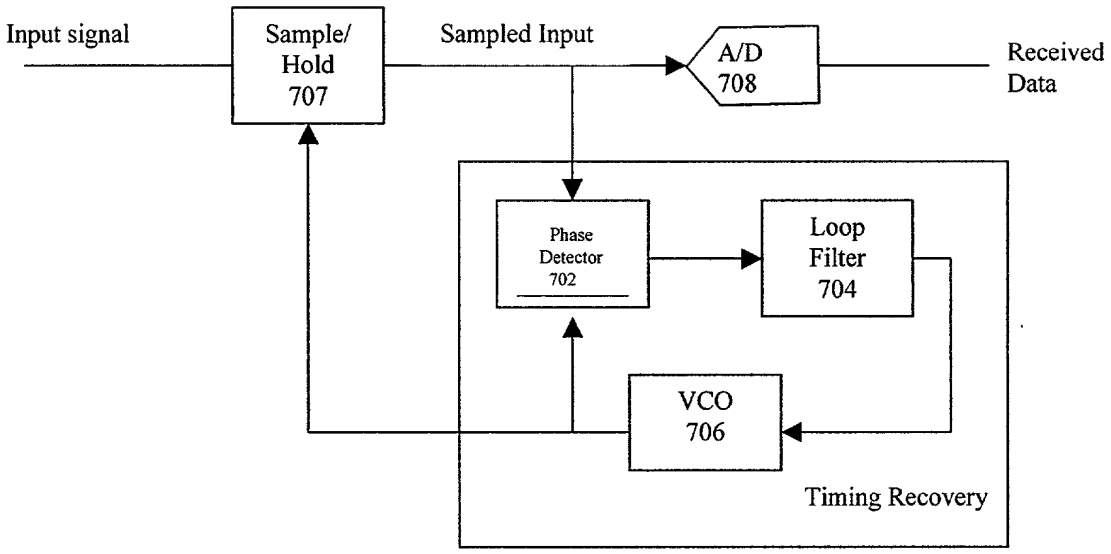
Input signal
Sample/
Hold
707
Sampled Input
A/D
708
Received
Data
Phase
Detector
702
Loop
Filter
704
VCO
706
Timing Recovery

FIG. 7

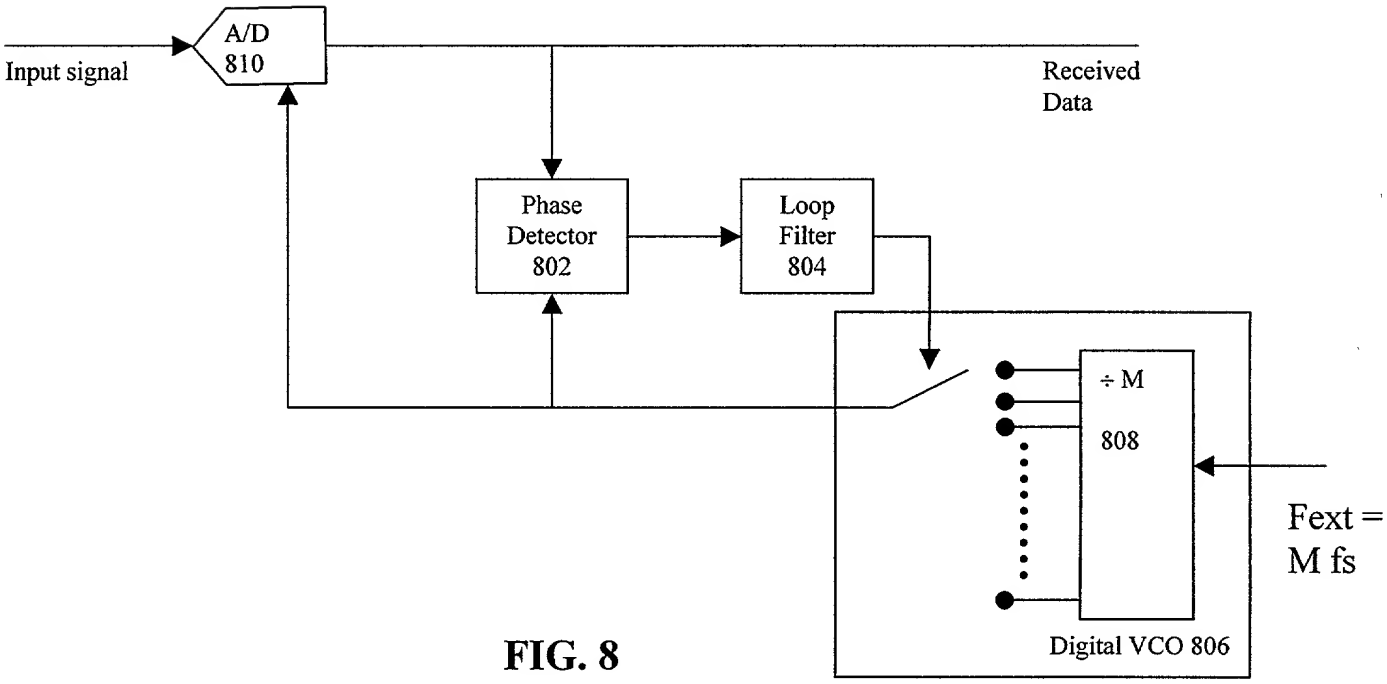
A/D
810
Input signal
Received
Data
Phase
Detector
802
Loop
Filter
804
÷ M
808
Fext =
M fs
Digital VCO 806

FIG. 8

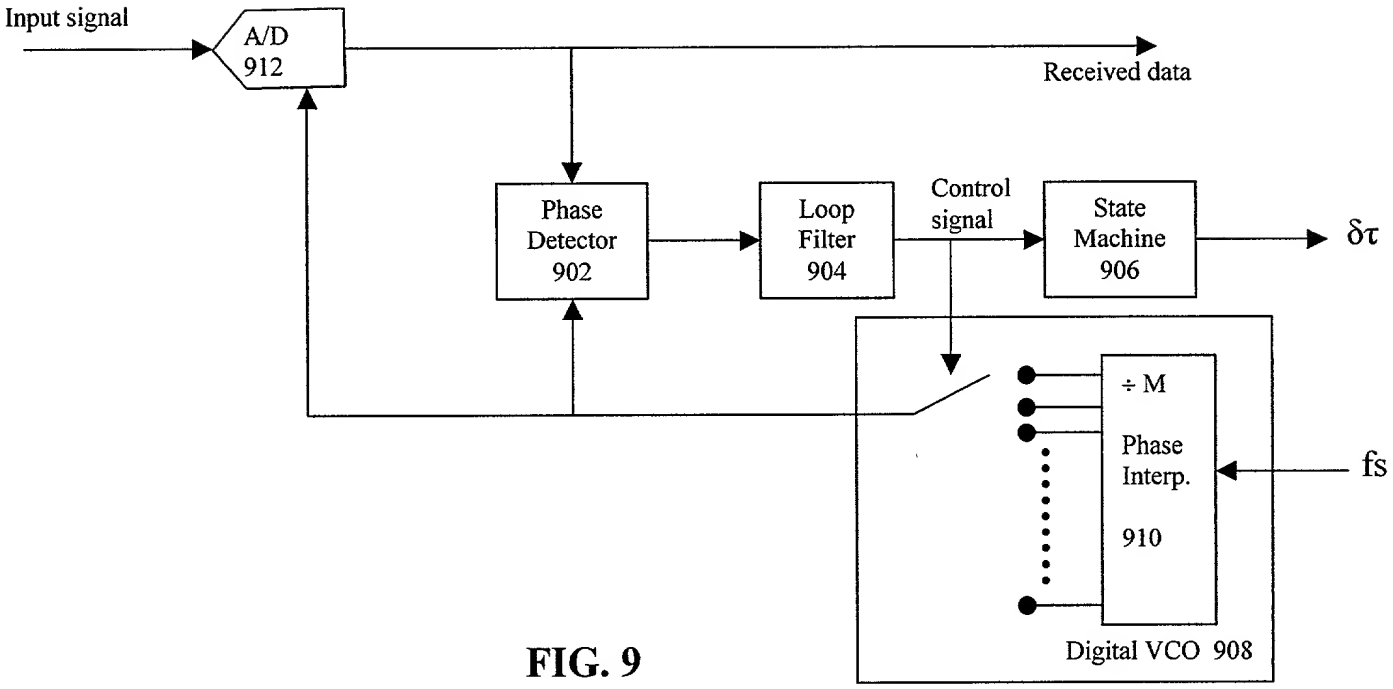
Input signal
A/D
912
Received data
Phase
Detector
902
Loop
Filter
904
Control
signal
State
Machine
906
δτ
÷ M
Phase
Interp.
910
fs
Digital VCO 908

FIG. 9

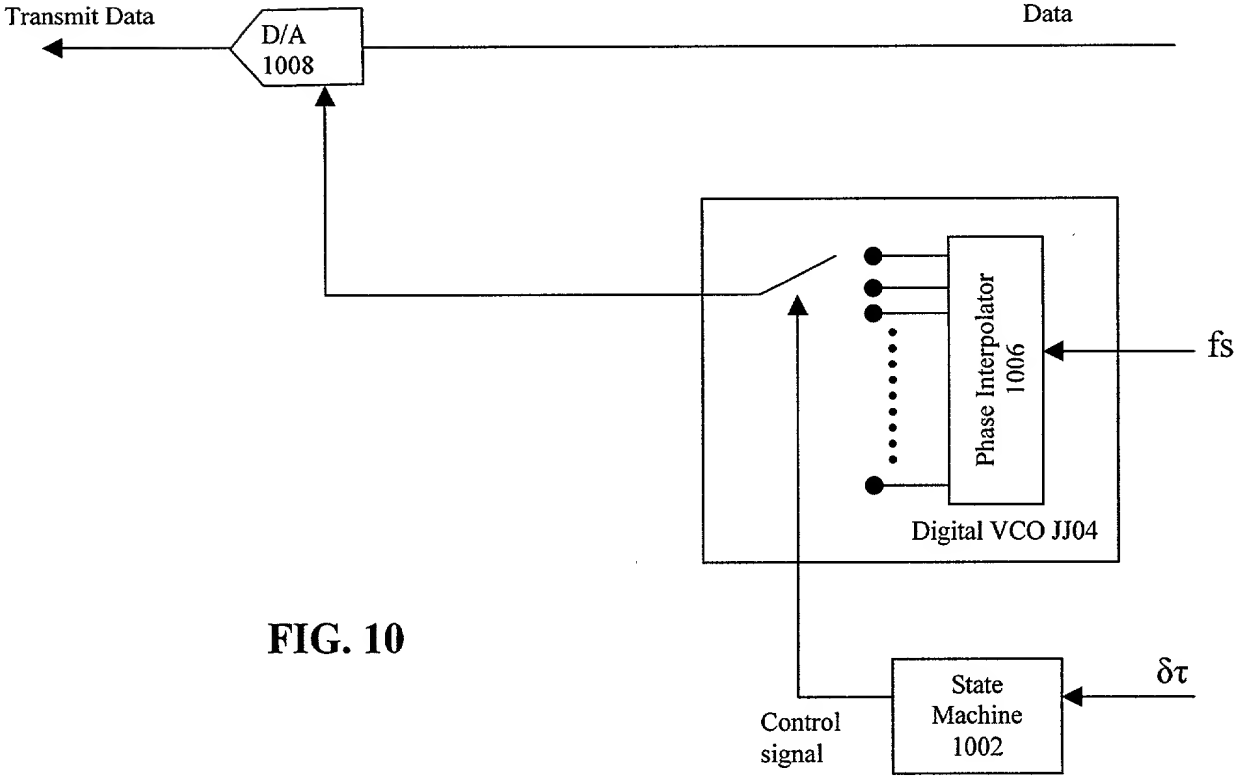
Transmit Data
D/A
1008
Data
Phase Interpolator
1006
fs
Digital VCO JJ04
Control
signal
State
Machine
1002
δτ

FIG. 10

RULE 63 (37 C.F.R. 1.63)
DECLARATION AND POWER OF ATTORNEY FOR PATENT APPLICATION
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

As a below named inventor, I hereby declare that my residence, post office address and citizenship are as stated below next to my name, and I believe I am an original, first and joint inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled **Method and Apparatus for Eliminating the Effects of Frequency Offsets in a Digital Communication System**, the specification of which is attached hereto, bearing Attorney Docket No. 73169 / 259697.

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims, as amended by any amendment referred to above. I acknowledge the duty to disclose all information known to me to be material to patentability as defined in 37 C.F.R. 1.56. I hereby claim foreign priority benefits under 35 U.S.C. 119/365 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate filed by me or my assignee disclosing the subject matter claimed in this application and having a filing date (1) before that of the application on which priority is claimed, or (2) if no priority claimed, before the filing date of this application:

PRIOR FOREIGN APPLICATION(S): Number	Country	Day/MONTH/Year Filed	Date first Laid-open or Published	Date Patented or Granted	Priority Claimed Yes [ ] No [ ]

I hereby claim domestic priority benefit under 35 U.S.C. 119/120/365 of the indicated United States applications listed below and PCT international applications listed above or below and, if this is a continuation-in-part (CIP) application, insofar as the subject matter disclosed and claimed in this application is in addition to that disclosed in such prior applications, I acknowledge the duty to disclose all information known to me to be material to patentability as defined in 37 C.F.R. 1.56 which became available between the filing date of each such prior application and the national or PCT international filing date of this application:

PRIOR U.S. PROVISIONAL, NONPROVISIONAL AND/OR PCT APPLICATION(S) Application No.:	Day/MONTH/Year Filed:	Status pending, abandoned, patented)	Priority Claimed? Yes [ ] No [ ]

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

And I hereby appoint Pillsbury Madison & Sutro LLP, 1100 New York Avenue, N.W., Ninth Floor, East Tower, Washington, D.C. 20005-3918, telephone number (650) 233-4790 (to whom all communications are to be directed), and the below-named persons (of the same address) individually and collectively my attorneys to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith and with the resulting patent, and I hereby authorize them to delete persons no longer with their firm and to act and rely on instructions from and communicate directly with the person/assignee who first sent this case to them and by whom I hereby declare that I have consented after full disclosure to be represented unless/until I instruct the above Firm and/or a below attorney in writing to the contrary.

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G. Lloyd Knight	17698	Glenn J. Perry	28458	Timothy J. Klima	34852	Anita M. Kirkpatrick	32617
Carl G. Love	18781	Kendrew H. Colton	30368	Stephen C. Glazier	31361	Michael R. Dzwonczyk	36787
Kevin E. Joyce	20508	Paul E. White, Jr.	32011	Paul F. McQuade	31542	W. Patrick Bengtsson	32456
George M. Sirilla	18221	G. Paul Edgell	24238	Ruth N. Morduch	31044	Jack S. Barufka	37087
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Country of Citizenship: Iran

Rule 56(a) & (b) = 37 C.F.R. 1.56(a) & (b)
PATENT AND TRADEMARK CASES - RULES OF PRACTICE
DUTY OF DISCLOSURE

(a) ... Each individual associated with the filing and prosecution of a patent application has a duty of candor and good faith in dealing with the [Patent and Trademark] Office, which includes a duty to disclose to the Office all information known to that individual to be material to patentability...(b) information is material to patentability when it is not cumulative and (1) It also establishes by itself, or in combination with other information, a prima facie case of unpatentability of a claim or (2) refers, or is inconsistent with, a position the applicant takes in: (i) Opposing an argument of unpatentability relied on by the Office, or (ii) Asserting an argument of patentability.

PATENT LAWS 35 U.S.C.

§102. Conditions for patentability; novelty and loss of right to patent

A person shall be entitled to a patent unless--

(a) the invention was known or used by others in this country, or patented or described in a printed publication in this or a foreign country, before the invention thereof by the applicant for patent or

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of the application for patent in the United States, or

(c) he has abandoned the invention, or

(d) the invention was first patented or caused to be patented, or was the subject of an inventor's certificate, by the applicant or his legal representatives or assigns in a foreign country prior to the date of the application for patent in this country on an application for patent or inventor's certificate filed more than twelve months* before the filing of the application in the United States, or

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent, or

(f) he did not himself invent the subject matter sought to be patented, or

(g) before the applicant's invention thereof the invention was made in this country by another who had not abandoned, suppressed, or concealed it. In determining priority of invention there shall be considered not only the respective dates of conception and reduction to practice of the invention, but also the reasonable diligence of one who was first to conceive and last to reduce to practice, from a time prior to conception by the other.

§103. Condition for patentability; non-obvious subject matter

A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made. Subject matter developed by another person, which qualified as prior art only under subsection (f) or (g) of section 102 of this title, shall not preclude patentability under this section where the subject matter and the claimed invention were, at the time the invention was made, owned by the same person or subject to an obligation of assignment to the same person.

* Six months for Design Applications (35 U.S.C. 172).